FIG. 1

BLOCK DIAGRAM SHOWING THE SCHEMATIC CONFIGURATION
OF A DISPLAY DEVICE ACCORDING TO EMBODIMENT 1

DD: DISPLAY PART

2: DATA DRIVER

1: DISPLAY CONTROLLER

FRAME MEMORY

3: GATE DRIVER

SS

FIG. 2

BLOCK DIAGRAM SHOWING THE DETAILED CONFIGURATION
OF A DISPLAY DEVICE ACCORDING TO EMBODIMENT 1

FIG. 3

TIMING CHART SHOWING A DRIVING STATE OF A
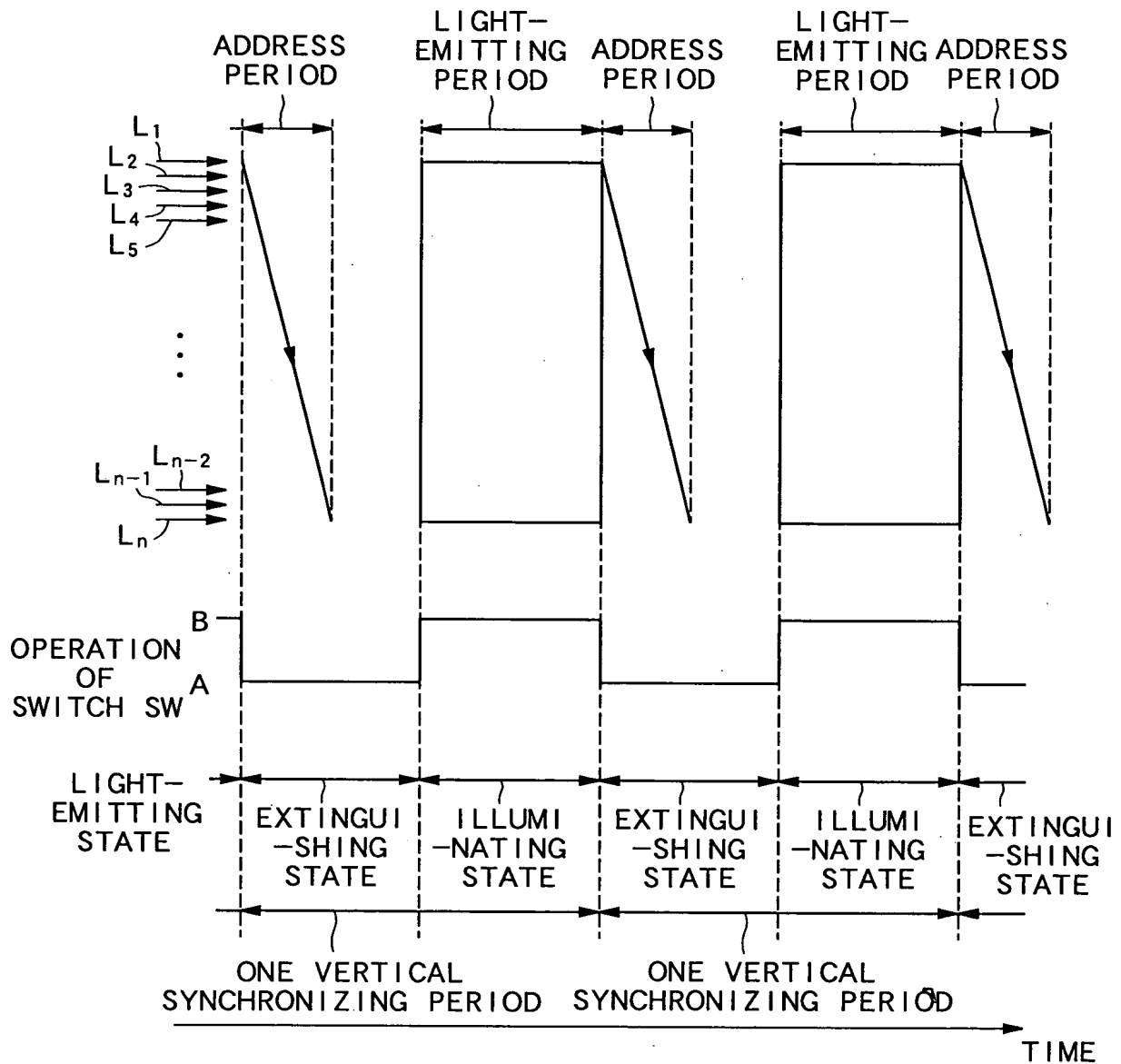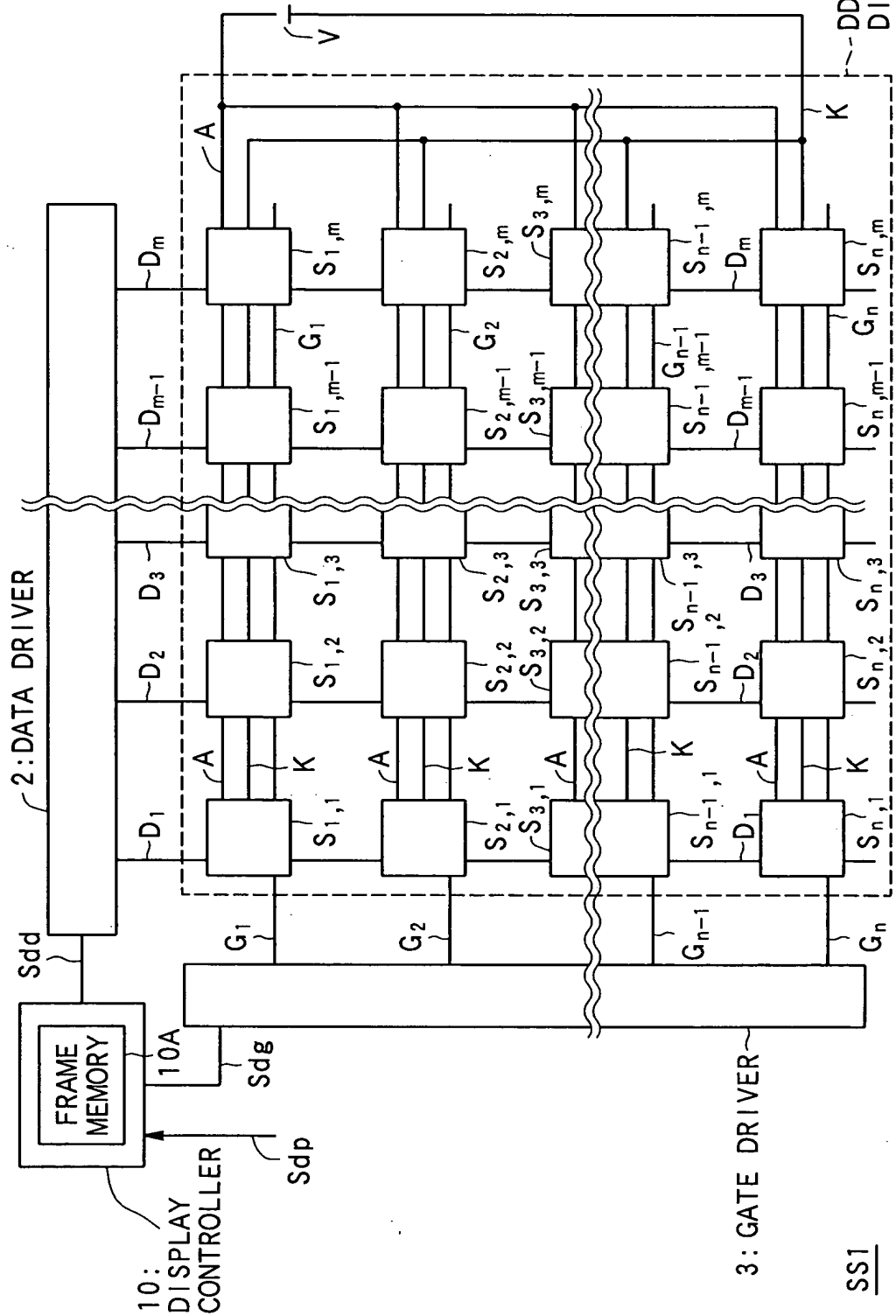DISPLAY DEVICE ACCORDING TO EMBODIMENT 1

FIG. 4

BLOCK DIAGRAM SHOWING THE SCHEMATIC CONFIGURATION
OF A DISPLAY DEVICE ACCORDING TO EMBODIMENT 2

FIG. 5

TIMING CHART (I) SHOWING A DRIVING STATE OF A
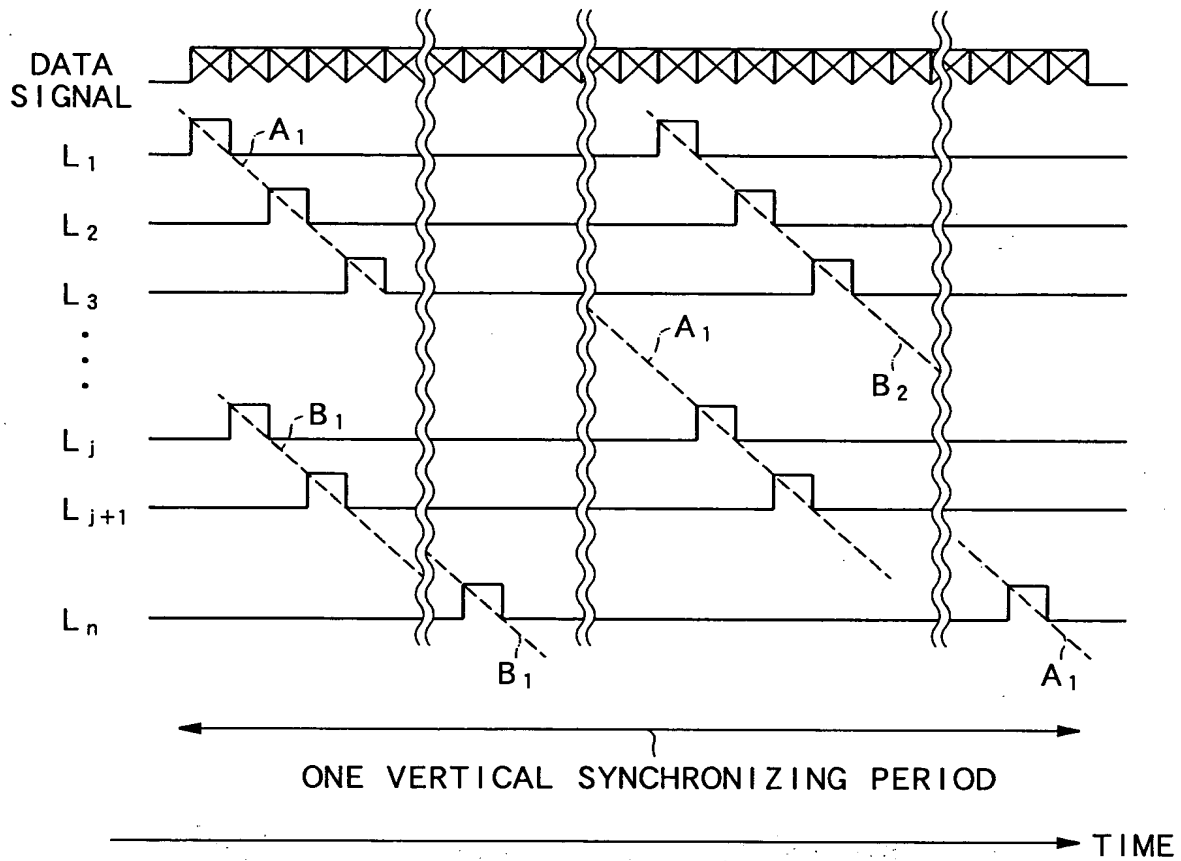DISPLAY DEVICE ACCORDING TO EMBODIMENT 2

ONE VERTICAL SYNCHRONIZING PERIOD

TIME

FIG. 6

TIMING CHART (II) SHOWING A DRIVING STATE OF A
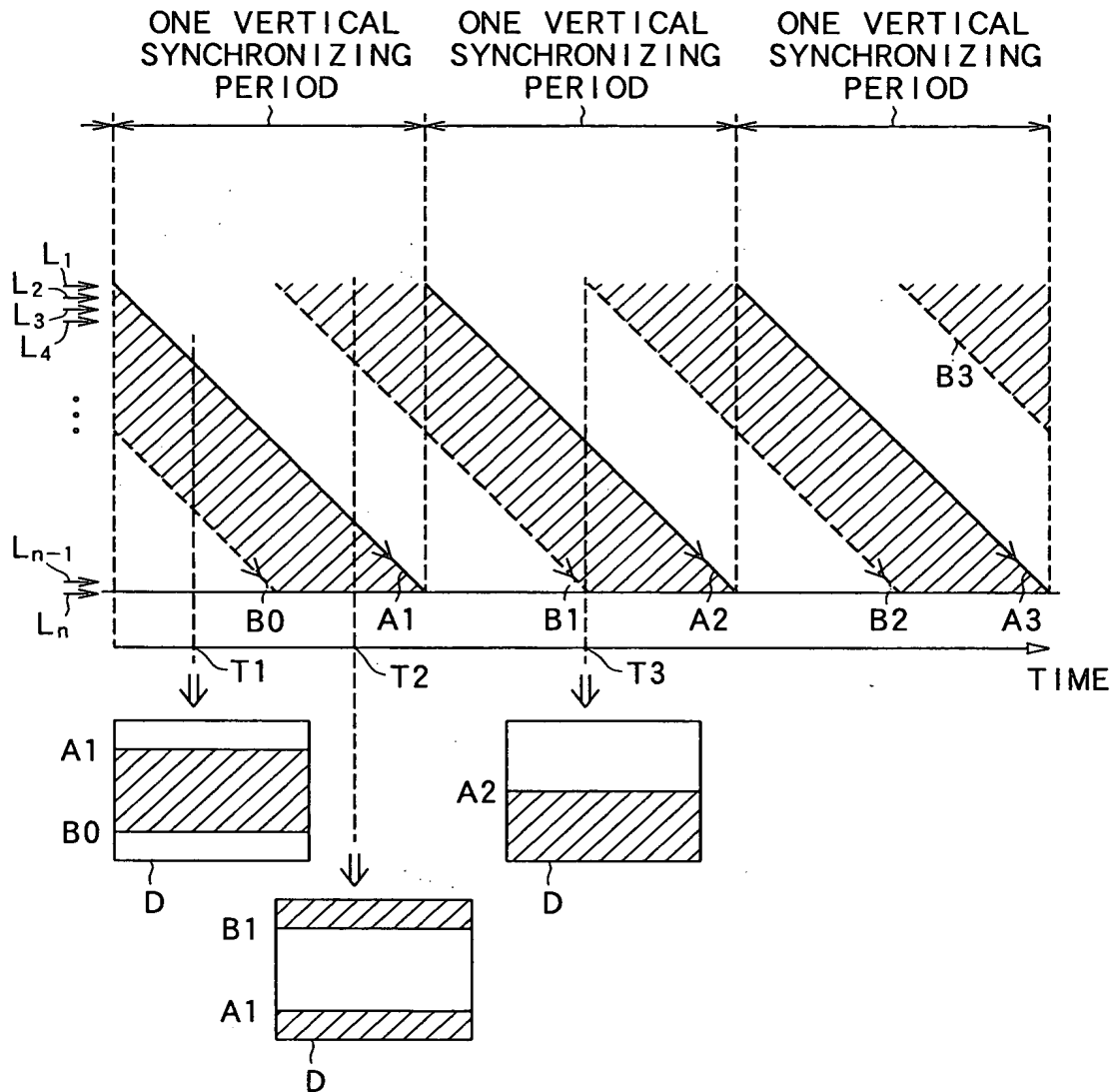DISPLAY DEVICE ACCORDING TO EMBODIMENT 2

FIG. 7

TIMING CHART SHOWING A DRIVING STATE OF A
DISPLAY DEVICE ACCORDING TO EMBODIMENT 3